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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,258	07/11/2003	Mitsuhiko Kitagawa	81788.0252	3224
26021	7590 06/07/2005		EXAMINER	
HOGAN & HARTSON L.L.P. 500 S. GRAND AVENUE			PHAM, LONG	
SUITE 1900	DAVENUE		ART UNIT	PAPER NUMBER
LOS ANGELI	ES, CA 90071-2611		2814	

DATE MAILED: 06/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			_KK
	Application No.	Applicant(s)	
	10/618,258	KITAGAWA ET AL.	
Office Action Summary	Examiner	Art Unit	
	Long Pham	2814	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet	with the correspondence address	
A SHORTENED STATUTORY PERIOD FOR RE. THE MAILING DATE OF THIS COMMUNICATIO  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a  - If NO period for reply is specified above, the maximum statutory per  - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may reply within the statutory minimum of the tion will apply and will expire SIX (6) Monthly that the cause the application to become	a reply be timely filed  nirty (30) days will be considered timely.  DNTHS from the mailing date of this communicated the mailing date of this communicated.	ation.
Status			
<ul> <li>1) Responsive to communication(s) filed on 29</li> <li>2a) This action is FINAL.</li> <li>2b) T</li> <li>3) Since this application is in condition for allocal closed in accordance with the practice under the condition of the condition</li></ul>	his action is non-final. wance except for formal ma		s is
Disposition of Claims			
4) ☐ Claim(s) 1-34 is/are pending in the application 4a) Of the above claim(s) 5-8 and 11-31 is/at 5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) 1-3,9 and 32-34 is/are rejected.  7) ☐ Claim(s) 4 and 10 is/are objected to.  8) ☐ Claim(s) are subject to restriction and	are withdrawn from conside	ration.	
Application Papers			
9) The specification is objected to by the Exam  10) The drawing(s) filed on is/are: a) a  Applicant may not request that any objection to the Replacement drawing sheet(s) including the constant of th	accepted or b) objected to the drawing(s) be held in abey rection is required if the drawin	ance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 CFR 1.12	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for fore  a) All b) Some * c) None of:  1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the papplication from the International Bur  * See the attached detailed Office action for a	ents have been received. ents have been received in priority documents have been reau (PCT Rule 17.2(a)).	Application No en received in this National Stage	
Attachment(s)  1)  Notice of References Cited (PTO-892)	A) ☐ Intervies	v Summary (PTO-413)	
<ul> <li>Notice of Notice Sched (170-032)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB. Paper No(s)/Mail Date</li> </ul>	Paper N	o(s)/Mail Date f Informal Patent Application (PTO-152)	

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#### **DETAILED ACTION**

## Drawings -

1. The drawings were received on 03/29/05. These drawings are approved.

# Claim Rejections - 35 USC § 103

- 1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 1. Claims 1, 2, 9, 32, 33, and 34 are rejected under 35 U.S.C. 103(a) as being unpatentable over the applicants' admitted prior art (AAPA) of this application in combination with Hiyoshi (US 5,734,703) and Umeji (US 4,614,873).

With respect to claim 1, AAPA teaches a MEMS apparatus comprising (see The Related Art on pages 1-2 and figs. 12 and 13A-13E of this application): a MEMS assembly driven by a voltage.

AAPA fails to teach that the voltage is generated by a light-receiving circuit or device that receives light from a light emitting circuit or device.

Hiyoshi teaches using a light-receiving circuit or device that receives light from a light emitting circuit or device to generate voltage. See col. 35, lines 58-65.

It would have been obvious to one of <u>ordinary skill</u> in the art of making semiconductor devices to generate voltage for the apparatus of AAPA as taught by Hiyoshi because the generated voltage can be controlled by controlling the intensity of the received light. See col. 35, lines 58-65.

Further with respect to claim 1, Hiyoshi fails to teach that the light-receiving circuit has series-connected light receiving devices.

Umeji teaches connecting light receiving devices or elements in series to control the voltage. See col. 1, lines 60-68.

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It would have been obvious to one of <u>ordinary skill</u> in the art of making semiconductor devices to connect the light receiving devices or elements in series in the apparatus of AAPA and Hiyoshi to obtain the above benefit.

With respect to claim 2, AAPA further teaches the MEMS assembly includes an RF-MEMS switch. See the Related Art on pages 1-2 and figs. 12 and 13A-13E of this application.

With respect to claim 9, AAPA also fails to teach that the MEMS apparatus includes a voltage discharge circuit.

However, the inclusion of a voltage discharge circuit is well-known to one of ordinary skill in the art of making semiconductor devices.

With respect to claim 32, AAPA also fails to teach that the MEMS apparatus includes a MEMS mirror.

However, the inclusion of a MEMS mirror is well-known to one of <u>ordinary</u> skill in the art of making semiconductor devices.

With respect to claim 33, AAPA also fails to teach that the MEMS apparatus includes a MEMS optical switch.

However, the inclusion of a MEMS optical switch is well-known to one of ordinary skill in the art of making semiconductor devices.

With respect to claim 34, AAPA also fails to teach that the MEMS apparatus includes a MEMS actuator.

However, the inclusion of a MEMS actuator is well-known to one of <u>ordinary</u> skill in the art of making semiconductor devices.

#### Response to Arguments

2. Applicant's arguments filed 03/29/05 have been fully considered but they are not persuasive. See below.

In response to the applicant's arugments in the paragraph at the middle of page 10 of the response dated 03/29/05, it is submitted that Hiyoshi teaches using

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a light-receiving circuit or device that receives light from a light emitting circuit or device to generate voltage that can be controlled.

In response to the applicant's arugments in the paragraph at the bottom of page 10 of the response dated 03/29/05, it is submitted that the Umeji is being relied on only for the teaching of connecting light emitting devices in series to control the voltage. Further, it is submitted that AAPA in combination with Hiyoshi teach a light-receiving circuit or device that receives light from a light emitting circuit or device to generate voltage.

# Allowable Subject Matter

3. Claims 4 and 10 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

## Conclusion

4. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Long Pham whose telephone number is 571-272-1714. The examiner can normally be reached on M-F, 7:30AM-3:00PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

løng Pkam

**Primary Examiner** 

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